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;; FILENAME: TenthSecondTimer.inc

;; Version: 2.6, Updated on 2015/3/4 at 22:27:47

;; Generated by PSoC Designer 5.4.3191

;;

;; DESCRIPTION: Assembler declarations for the Timer16 user module interface

;;-----------------------------------------------------------------------------

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include "m8c.inc"

;--------------------------------------------------

; Constants for TenthSecondTimer API's.

;--------------------------------------------------

TenthSecondTimer\_CONTROL\_REG\_START\_BIT: equ 0x01 ; Control register start bit

TenthSecondTimer\_INT\_REG: equ 0x0e1

TenthSecondTimer\_INT\_MASK: equ 0x02

;--------------------------------------------------

; Constants for TenthSecondTimer user defined values

;--------------------------------------------------

TenthSecondTimer\_PERIOD: equ 0xccd

TenthSecondTimer\_COMPARE\_VALUE: equ 0x0

;--------------------------------------------------

; Register Address Constants for TenthSecondTimer

;--------------------------------------------------

TenthSecondTimer\_COUNTER\_LSB\_REG: equ 0x20 ; DR0 Counter register

TenthSecondTimer\_COUNTER\_MSB\_REG: equ 0x24

TenthSecondTimer\_PERIOD\_LSB\_REG: equ 0x21 ; DR1 Period register

TenthSecondTimer\_PERIOD\_MSB\_REG: equ 0x25

TenthSecondTimer\_COMPARE\_LSB\_REG: equ 0x22 ; DR2 CompareValue register

TenthSecondTimer\_COMPARE\_MSB\_REG: equ 0x26

TenthSecondTimer\_CONTROL\_LSB\_REG: equ 0x23 ; Control register

TenthSecondTimer\_CONTROL\_MSB\_REG: equ 0x27

TenthSecondTimer\_FUNC\_LSB\_REG: equ 0x20 ; Function register

TenthSecondTimer\_FUNC\_MSB\_REG: equ 0x24

TenthSecondTimer\_INPUT\_LSB\_REG: equ 0x21 ; Input register

TenthSecondTimer\_INPUT\_MSB\_REG: equ 0x25

TenthSecondTimer\_OUTPUT\_LSB\_REG: equ 0x22 ; Output register

TenthSecondTimer\_OUTPUT\_MSB\_REG: equ 0x26

;--------------------------------------------------

; TenthSecondTimer Macro 'Functions'

;--------------------------------------------------

macro TenthSecondTimer\_Start\_M

or reg[TenthSecondTimer\_CONTROL\_LSB\_REG], TenthSecondTimer\_CONTROL\_REG\_START\_BIT

endm

macro TenthSecondTimer\_Stop\_M

and reg[TenthSecondTimer\_CONTROL\_LSB\_REG], ~TenthSecondTimer\_CONTROL\_REG\_START\_BIT

endm

macro TenthSecondTimer\_EnableInt\_M

M8C\_EnableIntMask TenthSecondTimer\_INT\_REG, TenthSecondTimer\_INT\_MASK

endm

macro TenthSecondTimer\_DisableInt\_M

M8C\_DisableIntMask TenthSecondTimer\_INT\_REG, TenthSecondTimer\_INT\_MASK

endm

; end of file TenthSecondTimer.inc